


ACKNOWLEDGEMENT RECEIPT

Electronic Version

Stylesheet Version v01

Title of Invention	Stacking Memory Chips Using Flat Lead-Frame with Breakaway Insertion Pins and Pin-to-Pin Bridges																																																																																																																														
Submission Type :	Utility Patent Filing																																																																																																																														
Application Number:	10/707624																																																																																																																														
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Server Response:	<table border="1"> <thead> <tr> <th>Confirmation Code</th> <th>Message</th> </tr> </thead> <tbody> <tr> <td>ISVR1</td> <td>Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application</td> </tr> <tr> <td>ICON1</td> <td>1623</td> </tr> </tbody> </table>			Confirmation Code	Message	ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application	ICON1	1623																																																																																																																						
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First Named Applicant:	Ren-Kang Chiou																																																																																																																														
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